

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 08-139524

(43)Date of publication of application : 31.05.1996

(51)Int.Cl.

H03D 7/00

(21)Application number : 07-235847

(71)Applicant : TOSHIBA CORP

(22)Date of filing : 13.09.1995

(72)Inventor : YOSHIDA HIROSHI

OGURA MIYUKI

IINO KOJI

OTAKA SHOJI

(30)Priority

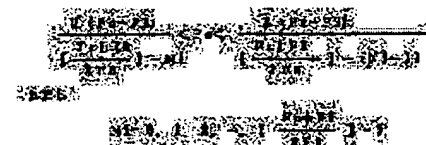
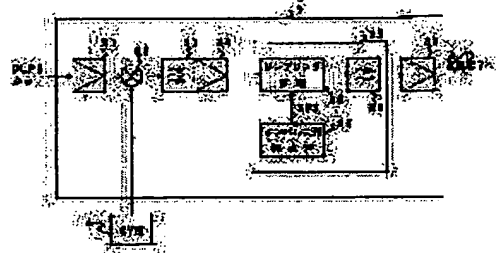
Priority number : 06221228 Priority date : 16.09.1994 Priority country : JP

(54) FREQUENCY CONVERTING CIRCUIT AND RADIO COMMUNICATION DEVICE EQUIPPED WITH SAME

(57)Abstract:

PURPOSE: To prevent signals in plural frequency ranges from being superposed before conversion by sampling a 1st received intermediate frequency signal by a sampling circuit according to an impulse sequence of frequency meeting specific requirements.

CONSTITUTION: A 1st received intermediate frequency signal passing through the mixer 32, etc., of a receiving circuit 3 is sampled by the sampling circuit 36 of a frequency converting circuit 35, and the circuit 36 samples the input signal with the impulse sequence SP1 of frequency F_s , outputted by an impulse sequence generator 37, which meets the conditions shown by the inequality. This sampling corresponds to the convolution between the frequency spectrum of the 1st received intermediate frequency signal and the frequency spectrum of the impulse sequence SP1, so plural spectrum appear at specific intervals, and the spectrum of the lowest center frequency passes through an LPF 38 to become a 2nd received intermediate frequency signal which has a sufficient signal level while frequency signals before frequency conversion are prevented from being superposed. In the inequality, F_C and F_B are the center frequency and band of the input signal.



LEGAL STATUS

[Date of request for examination]

02.06.1999

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application]

converted registration]

[Date of final disposal for application]

[Patent number] 3178997

[Date of registration] 13.04.2001

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office